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ARITHMETIC
INSTRUCTIONS

~~80386 DATA~~

~~MOVEMENT~~

~~INSTRUCTIONS Day 1~~

~~Part 1: Introductory~~

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~~Intel x86: Architecture,
Assembly, Applications
8086 Microprocessor
Architecture~~ Bharat
Acharya Register
Organisation Of
80386(Segment Select
Registers) ~~Design Your
Own CPU Instruction
Set~~

Intro to x86 Assembly
Language (Part 1) ~~80386
Introduction~~ Bharat
Acharya Education

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Breaking the x86

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~~an instruction set?~~

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CPU - Computerphile

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- OPEN BOX Education

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microprocessor 8086
part 1~~ x86 Assembly -
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Memory Banking |

Bharat Acharya

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~~Registers|by Prof.~~

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Organization

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Instructions | ADD,

ADC etc | Bharat

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| Instruction Set | Data
Transfer Group | Bharat

Acharya Education |

Gate Exam Mod-03

Lec-03 Instruction Set

Architecture 80386

Instruction Set

Chapter 17 80386

Instruction Set. This

chapter presents

instructions for the

80386 in alphabetical

order. For each

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instruction, the forms are given for each operand combination, including object code produced, operands required, execution time, and a description. For each instruction, there is an operational description and a summary of exceptions generated.

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Chapter 17

Chapter 17 80386

Instruction Set This

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order. For each

instruction, the forms

are given for each

operand combination,

including object code

produced, operands

required, execution

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time, and a description.

For each instruction, there is an operational description and a summary of ...

Chapter 17 80386

Instruction Set - logix.cz

The Intel 80386, also known as i386 or just 386, is a 32-bit microprocessor introduced in 1985. The first versions had

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275,000 transistors and were the CPU of many workstations and high-end personal computers of the time. As the original implementation of the 32-bit extension of the 80286 architecture, the 80386 instruction set, programming model, and binary encodings are still the common denominator for all

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32-bit x86 processors,
which is termed the
i386-architecture, x86,
or IA-32, depending o

Intel 80386 - Wikipedia

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INSTRUCTION SET

OF A 80386

PROCESSOR 1. Bit

Scan Instructions:

80386 instruction set

has two bit scan

mnemonics, such as

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BSF (bit scan forward)
and BSR (bit... 2. Bit
Test Instructions: 80386
have four bit test
instructions, those are
BT (test a bit), BTC
(test a bit and... 3.
Conditional ...

MICROPROCESSORS:
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INSTRUCTION SET
OF A 80386
PROCESSOR

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16.1 How the 80386
Implements 16-Bit and
32-Bit Features; 16.2
Mixing 32-Bit and
16-Bit Operations; 16.3
Sharing Data Segments
Among Mixed Code
Segments; 16.4
Transferring Control
Among Mixed Code
Segments> Part IV
Instructions Set Chapter
17 -- 80386 Instruction
Set. 17.1 Operand-Size
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and Address-Size Set

Attributes; 17.2

Instruction Format.

Appendices

80386 Programmer's

Reference Manual --

Table of Contents

The 8086 / 80286 /

80386 / 80486

Instruction Set This

HTML version of the

file intel.doc from the

PC Game Programmer's

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guide was produced by

Zack Smith,

fbui@comcast.net. ... If

CF is set, a 1 is added to

the destination. Clocks

Size Operands 808x 286

386 486 Bytes reg,reg 3

2 2 1 2 ...

The 8086 / 80286 /

80386 / 80486

Instruction Set

80386 Instruction Set 1.

Arithmetic Instructions.

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There are two new sign extension instructions beyond those of the 8086. CWDE Sign-extend 16... 2. Bit Instructions. BSF scans (checks) the 16-bit (word) or 32-bit (double word) number defined by s from right to left... 3. Set Byte on Condition ...

80386 Instruction Set ~

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8051 microcontrollers

INTEL 80386

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PROGRAMMER'S

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MANUAL 1986

The updated instruction set is also grouped according to

architecture (i386, i486,

i686) and more generally is referred to as x86 32 and x86 64

(also known as AMD64). Original 8086/8088 instructions

Added in specific

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processors Added with
80186 / 80188

x86 instruction listings -
Wikipedia

Following is the list of
instructions under this
group

- REP Used to
repeat the given
instruction till CX = 0.
- REPE/REPZ Used to
repeat the given
instruction until CX = 0
or zero flag ZF = 1.

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REPNE/REPNZ □ Used to repeat the given instruction until $CX = 0$ or zero flag $ZF = 1$.

Microprocessor - 8086

Instruction Sets -

Tutorialspoint

17.2.2 How to Read the

Instruction Set Pages

The following is an

example of the format

used for each 80386

instruction description

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in this chapter: CMC --
Complement Carry Flag
Opcode Instruction
Clocks Description F5
CMC 2 Complement
carry flag

80386 Programmer's
Reference Manual --
Section 17.2

 They gave up,
when Pentium solved
the issue (by internally
translating CISC ->

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RISC). en lieu et place

de leur contrepartie en

16 bits (ax, bx, etc.)..

That also failed — big

time — against the

simpler upgrade path

offered by AMD. The

multiplication

instructions set carry

and overflow if the

result is larger than the

small value register.

Although the 80386

does not perform out-of

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... Instruction Set

intel 80386 instruction
set - dscvr-app.com

Returning from a Nested Task: If you execute an IRET instruction when the NT (Nested Task) flag in EFLAGS is set, the 80386 returns to your "parent" task. However, it will neglect to clear NT in the outgoing task's TSS.

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Instruction Set

Intel 80386 CPU

Information | PCs

Machines

An 80386 data-manipulation instruction that accesses memory uses one of the following methods for specifying the offset of a memory operand within its segment: Most data-manipulation instructions that access

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memory contain a byte that explicitly specifies the addressing method for the operand.

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Reference Manual --
Section 2.5

This section presents an overview of the 80386 processor as it applies to assembly language programming. Most books you will find on

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this subject are geared toward PC based assembly language products using the Intel 803x6 instruction set. The GNU assembler (GAS), which DJGPP uses, is based on the AT&T 680x0 instruction set.

Guide: About the 80386 architecture

LOOP/LOOPcond --

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Instruction Set

Loop Control with CX

Counter Opcode

Instruction Clocks

Description E2 cb

LOOP rel8 11+m DEC

count; jump short if

count > 0 E1 cb LOOPE

rel8 11+m DEC count;

jump short if count > 0

and ZF=1 E1 cb

LOOPZ rel8 11+m DEC

count; jump short if

count > 0 and ZF=1 E0

cb LOOPNE rel8 11+m

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DEC instruction; jump short

if count > 0 and ZF=0

E0 cb LOOPNZ rel8

11+m DEC count; jump

short if count > 0 ...

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Reference Manual --

Opcode LOOP

The 80x86 family

provides five logical

instructions, four rotate

instructions, and three

shift instructions. The

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logical instructions are and, The 80386 and later processors provide an even richer set of operations. bts, btr, btc, bsf, bsr, shld, shrd, and the conditional

CHAPTER SIX: THE 80x86 INSTRUCTION SET (Part 3)

The Instructions used for data transfer in I/O mapped I/O are a) IN,

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OUT b) IN, LDA add c)

STA add d) None of the

above 20. Number of

Address lines in 80486

is a) 16 b)32 c) 34 d)

128 REVIEW

QUESTIONS 2

MARKS 1. Differentiate

between 80386 and

80486. 2. Classify the

different groups of

80386 instruction set

with example. 3.

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313d02fe8fa03b3a43e4
1fdf94