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~~Computer Logic Design M Morris Mano Part 2 Q. 1.33: The state of a 12-bit register is 100010010111. What is its content if it represents~~
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Q. 4.13: The adder-subtractor circuit of Fig. 4.20(b) has the following values for mode input M and *Standard Books for ISRO, IES* \u0026 GATE Preparation for ECE Digital Design 5th Edition Mano

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